ACADEMIC BULLETIN

Academic Year- 2022-2023

Department of Electronics & Telecommunication Engineering
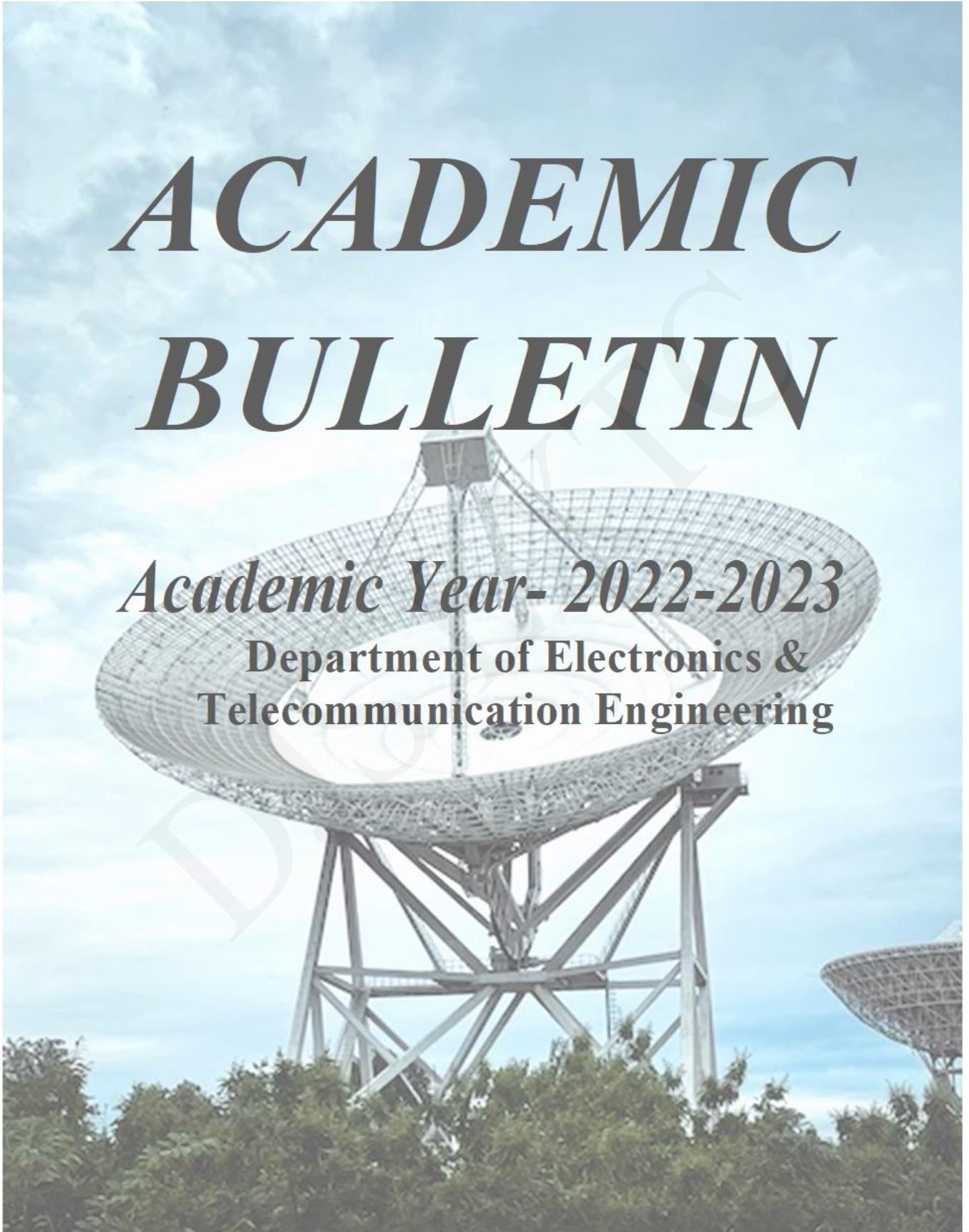
ACADEMIC BULLETIN

July 2022- December 2022

Department of Electronics & Telecommunication Engineering

Prepared By:

Dr. Poonam Kadam   **Dr. Amit A. Deshmukh**

(Assistant Professor, EXTC, DJSCE)   **(Professor & Head EXTC, DJSCE)**

ACADEMIC BULLETIN

Period: 1st July 2022 – 31st December 2022

1. ABOUT DEPARTMENT

1.1 Vision

To be a world class Institution for education, training and research in engineering, inculcatingvalues and skills for sustainable development of the society.

1.2 Mission

- To provide competent faculty and an interactive learning environment along with worldclass infrastructure for nurturing professionalism & entrepreneurship in Engineers.
- To foster technical competence, research aptitude and environmental awareness amongstaspiring technocrats to develop sustainable engineering solutions.
- To provide a forum for active interaction between academia & industry, leading tocontinuous improvement in engineering education.

1.3 Vision of the Department

To develop technically competent and socially responsible Electronics and Telecommunication engineers capable of fulfilling expectations at indigenous and global levels.

1.4 Mission of the Department

- To provide a conducive educational environment for students by providing goodinfrastructural facilities, knowledge base and excellent faculty support.
- To provide a strong foundation of core knowledge and exposure to research culture.
- To motivate learners to acquire adequate professional and soft skills, to develop personalitytraits and eventually transform them as life-long learners.
- To strive and achieve practical exposure by maintaining good rapport with industry and professional network.

1.5 Program Specific Outcomes (PSOs)

- To develop knowledge in the domain of signal analysis and processing and provide afoundation to numerous other courses that deals with signal processing applications.

- To develop basic and applied knowledge of the architecture and assembly language programming for microprocessor/microcontroller-based systems, along with the peripheralinterfacing.

- To provide an in-depth understanding of electromagnetics, transmission lines and antennaconcepts along with microwave devices used for RF and microwave applications.

- To develop knowledge of the fundamental techniques related to generation, transmission and reception in communication systems for a wide range of wired and wireless applications alongwith revolutionary technology developments.

1.6 Program Educational Objectives (PEOs)

- **PEO1:** To prepare learners for graduate studies by providing strong foundation of basic sciences, computer programing and thus, develop analytical aptitude, and problem-solving abilities.

- **PEO2:** To develop a fundamental understanding of electronic & integrated circuits, communication systems and allied disciplines.

- **PEO3:** To develop core competency and expertise in the diverse areas of communication covering Signal processing, Electromagnetic Engineering, Embedded Systems, ComputerCommunication and Advanced Wireless Networks domains.

- **PEO4:** To inculcate competencies and aptitude in extending acquired technical knowledge to solve real life issues with high professional and ethical standards.

- **PEO5:** To develop proficiency in soft skills and deliver adequate personality traits to enablethe pass outs to pursue higher education, to find competitive employment opportunities and/or pursue entrepreneurial ventures.

1.7 Department Information

- Started in the year 1999 with the intake of 30 and which was increased to 60 in the subsequentyear.

- The intake was increased to 120 in the Academic Year 2010 – 11.

- In the Academic Year 2011 – 12, Department has started M.E. Program in Electronics & telecommunication with an intake of 18 students.

- For the first time Department got NBA accreditation for two years from January 2013. In second Outcome based evaluation, Department got NBA accreditation for three years from July 2017.

- The Department started with Ph.D. program in Academic Year 2015 – 16 with an intake of 10 students.

- The department is having highly qualified, experienced and dedicated faculties and supportingstaff.

- Well-equipped labs and fully air-conditioned classrooms with projectors.

2. ADMINISTRATION

IETE COMMITTEE

Dr. Amit Deshmukh   Dr. Anuja Odhekar

PROJECT COORDINATOR

Dr. Amit Deshmukh   Dr. Ameya Kadam

DEPARTMENTAL LIBRARY

Dr. Amit Deshmukh   Prof. Archana Chaudhari

ALUMNI COMMITTEE

Dr. Amit Deshmukh   Prof. Ranjushree Pal

NBA CORE COMMITTEE

Dr. Amit Deshmukh   Dr. V. V. Kelkar (PC/NC)

Dr. Ameya Kadam   Dr. Venkata A. P. Chavali

AUTONOMY COMMITTEE

Dr. S. B. Deshmukh   Prof. Archana Chaudhary

TIME-TABLE COMMITTEE

Dr. Poonam Kadam   Dr. Venkata APC

PLACEMENT COORDINATOR   NPTEL and IBM COORDINATOR

Dr. Aarti Ambekar   Dr. V. V. Kelkar

3. IETE- SF

The Electronics and Telecommunication Department of Dwarkadas. J. Sanghvi College of Engineering presents Institution of Electronics and Telecommunication Engineers- Student Forum (**IETE-SF**). The student chapter with a working force committee of 22, consisting of **second year**and third **year students**, hosted a few of the most quintessential and technically challenging events. A membership drive was conducted at the start of the year with an overwhelming response. (www.djsceietesf.com)

IETE Organizing Committee Structure

IETE SF Branch Counsellor: - Dr. Anuja Odhekar

Chairman	Kushal Katira
Vice-Chairman	Kenil Shah, Shrushti Thakar
Secretary	Sanika Dawre, Yash Shivramkrishnan
Treasurer	Shivam Desai
DJ-Strike Co-ordinator	Utsavi Lotia, Pranav Saraf, Urvashi Bhushan, Vidhi Tarak Nayak

Head Of Departments:	
Publicity	Sharath Pai
Marketing	Ayush Koul, Maahi Trivedi
Technical	Siddharth Nagaokar
Infotech	Sharmila Choudhary
Creatives	Sarthak Acharekar, Dhara Patel
Events	Hitarth Sharma
Book Bank	Disha Goel
Component Bank	Aditya Patil

3.1 Value Added Program

Book Bank

IETE-SF provides the students with a book bank facility where they can issue reference books at nominal rates for the entire semester. Students who want a better insight into the subject avail this facility as these reference books aid in developing a good understanding of the topics and enable them to consolidate their foundation of the subject. Book bank has more than 60 book titles as per the syllabus requirement for 3rd to 8th semester. This activity not only motivates students to use reference books prescribed in syllabus but also explore them in the library management system. The alumni who worked as "Book-Bank" coordinators received paid assistance-ship in the library during their master program.

Component Bank

IETE-SF provides a component bank facility where students can borrow electronic components which they require for executing multiple projects both in and outside of the curriculum. They can utilize the facility by initially paying 50% of the cost and getting a refund of 20% on returning the components, provided that they are undamaged.

4. DEPARTMENT ACTIVITIES UNDER IETE-SF

4.1 Workshop on MATLAB Programming

Speakers: Dr. Venkataramanan V and Prof. Ranjushree Pal

Association of the Speakers: Professor, DJSCE Mumbai

Date of the Session: 24th, 26th September 2022

No. of Participants: 81

Participants: SE and TE Students

Objectives of the activity:

- To make the students familiar with a new software.
- To make use of the MATLAB Programming commands to problems in different mathematical subjects.
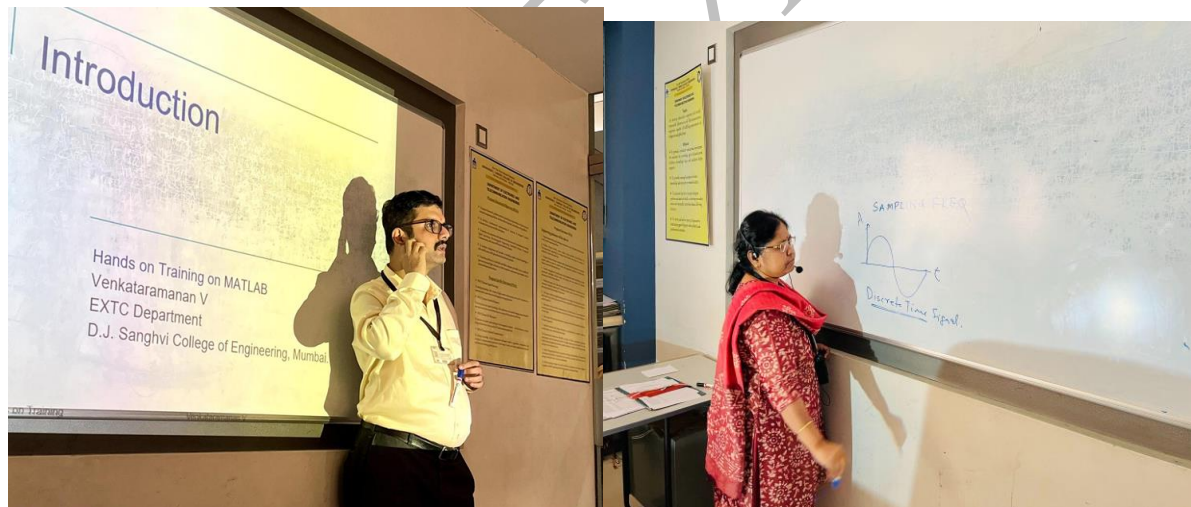
Contents:

MATLAB (Matrix Laboratory) is a simple programming language with its own extensive library of mathematical and graphical subroutines. MATLAB is the world's most successful piece of numerical analysis software. Cleve Moler, founder of MATLAB, in his interview outlined that he created MATLAB for educational purposes from a freely distributed package. MATLAB combines a desktop environment tuned for iterative analysis and design processes with a

programming language that expresses matrix and array mathematics directly. It includes the Live Editor for creating scripts that combine code, output, and formatted text into an executable notebook. It is one of the most versatile platforms which is useful in Data Analysis, Graphics, Parallel Computing, App Building and Web Development. The software also has a plethora of different utilities that are useful for projects involving app building, parallel computing, hardware i.e., embedded systems and several others. It is widely used in academia as well as in industry. Therefore, to make students aware of the MATLAB Programming Software, IETE-SF conducted two "Workshops on MATLAB Programming". It was a two-day event. The aim of the event was to give a brief introduction to MATLAB. The Sessions took place on 24th and 26th September 2022. The Sessions were very informative and interactive for the attendees. The Sessions started with good enthusiasm among the attendees to gain a good hold of the MATLAB Platform under the guidance of the Professors. The two sessions were broken into various parts: Brief Introduction to MATLAB GUI, Interactive Commands in MATLAB, Introduction to Operators, Variables, Datatypes, Introduction to Arrays & Functions, Colon Notation, Plotting, a Fun Gaming Session, Audio Processing, Introduction to Signals and Systems, Generation of aperiodic Signals, Fun Animation and so on.

The first day of the workshop was helmed by Dr. Prof. Venkataramanan who took on elementary concepts to build basic understanding amongst first time MATLAB users. He guided the students in launching the software and explained the entire interface in great detail. While giving Brief Introduction to MATLAB GUI, the Professor explained how to start MATLAB and explained all the components on the Workspace Window of the software. He also explained applications of these components to make the use of software more fluent. The students were also introduced to basic commands in MATLAB like 'clc' which is used to clear the command window, 'eye' which produces an identity matrix in the output and many others. He made everyone aware of the fact that MATLAB is case sensitive and how maintaining the proper case is imperative to getting an error free code. Next on the agenda was Interactive Commands in MATLAB. Interactive Commands includes multifarious commands like Commands for managing sessions, Commands for working with the system, Commands for Input/Output, Plotting Commands and so on. To make the workshop more interactive, the Professor then introduced Funny Commands like 'Why' to attendees, which throws Funny Jokes on running the command. Operator was the next, this was explained with the help of basic examples. For example, Arithmetic Operators,

Relational Operators, Logical Operators, etc. Students also tried these examples which helped them to learn Operators. Professor also explained how to write the Matrix. These Matrices included Row, Column & Square Matrices.

The Speaker then introduced basic operations on matrices and arrays. The students were made familiar with how conditional statements, functions, loops and switch statements can be used in MATLAB to get the desired output. Dr. Venkataraman explained that the method of using these utilities is almost the same as the C language. Professor explained various Data-Types available in MATLAB. In order to make the session more fun and interactive, the students were asked to input commands like 'image', 'images and Video', 'earthmap', 'wrldtrv' and 'spy'. These are fun little commands hidden in the software like easter eggs. The students had a great deal of fun while competing to complete the puzzle that is opened using the command fifteen. The group who solved the puzzle first became the winner and collected the prize. He also assisted students in making a wav file using MATLAB to play music. Overall, the session was highly engaging and all the students listened with rapt attention. It was extremely informative and helped build a foundational understanding of the software which would be furthered on day 2 of the MATLAB workshop. IETE-SF then thanked Dr. Venkatramanan for his valuable time and contribution and thus ended the workshop.

On day 2 of the workshop, the students were addressed by Prof. Ranjushree Pal. She started off the session by explaining the terms such as sampling frequency, sampling rate, and plotting time vector. Sampling is a process used to convert a continuous time signal to a discrete signal. The unit of sampling frequency is samples per second. The professor then enlightened the students about determining the graph by changing the value of frequency and then moved on to plotting 10 cycles of analog signal. The session was very factual and relevant to modern technology. The students were then informed about the Sampling frequency and 'Nyquist Frequency', use of a time vector at an appropriate rate to generate the signal. Later, the professor explained the rules of 'Shifting', 'Scaling' and 'Folding' and made clear the conditions for each of them. Every word and line of the code was clearly explained by the professor, which was thoroughly understood by all the students. The Professor then discussed the plotting of a rectangular pulse in the middle of the time axis. The students were then made aware of the 'sinc' formula and taught how to find frequency by 'Fast Fourier Transform' in MATLAB. Three distinct signals were shown, along with noise. The workshop proceeded further with the Impulse Response of a filter removing a

noise. It was an interactive session, advanced and a step ahead of the basic session. All the interesting and new things were taught to the students. "It was quite fascinating and enjoyable to grasp various academic concepts and also practising them first-hand using MATLAB was refreshing," was one of the points of view of the enthusiastic student. In conclusion, the session was very interesting and thought-provoking. IETE-SF then thanked Prof. Ranjushree Pal for her valuable time and contribution and thus ended the workshop.

Photographs of the Event:

Basics Hands-on Session Day-1
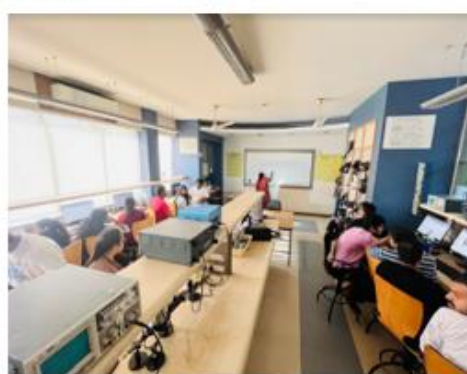
Advanced Hands-on Session day-2

Token of thanks

Outcomes:

- The students understood the need for MATLAB and how it works.

- They further understood how MATLAB works, its Workspace window and its GUI. Thus gaining knowledge of a versatile software.

- The students also got well versed with Operators, Data-Types, Arrays and Functions available on the software.

- The students also understood the Implementation of signals and systems on the software.

Program Outcomes mapped (Please tick the mapped POs):

PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
1	1	1	1	1	2	2	1	2	2	1	1

Program Specific Outcomes mapped (Please tick the mapped PSOs):

PSO1	PSO2	PSO3	PSO4
1	1	1	1

4.2 Workshop on Ethical Hacking

Speakers: Priya Savla, Hardik, Manav

Date of the session: 06th November and 13th November 2022

No. of Participants: 23

Participants: FE, SE and TE students

Objectives of the activity:

- To provide students with information about Ethical Hacking**.**

- To make students familiar with various software used in Ethical Hacking.

Contents:

Ethical hacking has become a necessity in today's world and is a very fast-growing field. Ethical Hacking offers various professional development opportunities and is a good way to kick start a career in the highly accretive field of Cyber Security. Therefore, to make students aware of this field, IETE-SF conducted a "Workshop on Ethical Hacking". It was a two-day event. The aim of the event was to give a brief introduction to the field of Ethical Hacking. The Sessions took place on 06th and 13th November 2022. The Sessions were very informative and interactive for the attendees. The Sessions started with good enthusiasm among the attendees to gain a good grasp of Ethical Hacking under the guidance of the Speakers. The first day of the workshop was helmed by Ms. Priya Savla, who is a Cyber Security and Digital forensics expert. She began the session with the basics of Ethical hacking terms, such as the importance of 'Ethical' in the phrase 'Certified Ethical Hacker (CEH)'. The participants were informed about the difference between 'hacking' and 'ethical hacking'. The

difference between the terms is that ethical hacking is all about loopholes, intention and permission. Ethical hacking is actually aligned with every other domain, such Artificial Intelligence, Data Science and Machine Learning.

The first thing that she taught was a simple trick to hack into passwords without any tools or software. Any password can be looked into without much effort, so saving your passwords is not such a bright idea, but a person possibly can't remember all of their passwords, so the smart way to avoid getting your password hacked would be to change your passwords every once in a while. She made the attendees aware of how the mind of a hacker works and what they could do from their end to avoid it. They then moved on to how one can view the SSID list of all the WiFi that the device has been connected to, which is possible with the software 'wireless-key-view'. She discussed how even simple things like WIFI can be abused by hackers and advised students on how to protect their devices and data by implementing a few simple but effective layers of protection.

Later, she educated the students on the importance of IP Addresses and how networking and cybersecurity go hand in hand. The students then dived into software such as 'who.is', 'whatismyipaddress.com', 'myip.ms' through which one can easily find out their IP Address. She then instructed the students on how one's identity can be protected while surfing the 'dark net' which is the hacker's world. The students were then made aware of the 'Tor Browser' and how the browser has 3 hops and a normal VPN has 1, hence getting 3 layers of protection. She informed us that there is a public IP Address that can only be viewed by you and a Public IP Address that can be viewed by anyone and everyone. We saw where we could access our private IP Address from. After that, in order to make the session more fun, the students played a fun little game using the software 'Our-Secret'. Our-Secret is a software that lets you hide text, images, video, and audio in a file. The message is password protected and is passed on from one person to another through a riddle and the answer to that riddle is the password to the sensitive information hidden inside the file. They were supposed to hide a message in a picture and write the riddle on the notepad file, then we were swapped and we were supposed to crack the riddle. It was a fun little game that made the session interesting and lightened everyone's mood. The students then explored what most of them were excited about, the 'Kali Linux' Software. Kali Linux is the hacker's Operating System. They then exhibited all the settings of the software. After installing the software the speaker taught the basic Linux commands like 'mkdir', 'ls', 'cd', 'pwd', 'man', 'mv', etc.Overall, the session was highly engaging and all the students listened with rapt attention. It was extremely informative and helped build a foundational understanding of Ethical

hacking which would be furthered on day 2 of the workshop. IETE-SF then thanked Ms. Priya for her valuable time and contribution and thus ended the workshop.

The second day of the workshop was conducted by Hardik and Manav. Hardik is a distinguished Information Analyst. On that day i.e., 13th November 2022, they began the session with the difference between 'HTTP' and 'HTTPS'. Data is transferred in plain text over HTTP, which is not secure, whereas data is encrypted over HTTPS. They displayed how hackers attack public WiFi and how it is more vulnerable. The students were then introduced to ports and their significance. There are a total of 65,535 ports. For example, the port number for HTTPS is 443, HTTP is 80, FTP is 21, SSH is 22, etc. They then taught the attendees about the HTTP status codes. An HTTP status code is a message a website's server sends to the browser to indicate whether or not that request can be fulfilled. There are a total of 200 status codes. The 1 X denotes the International Code, the 2X denotes the Successful Code, the 3X denotes the Redirection Code, the 4X denotes the Client Error Code, and the 5X denotes the Server Error Code. For example, Error 502 indicates a Flooding of the server or a DOS Attack. They then explained the DOS Attack and the DDOS Attack. The DOS Attack occurs from only one machine but the DDOS attack occurs from multiple machines. Later, the speakers enlightened the students about what exactly happens after they type 'google.com' and before they get the result. After that, they described what port scanning is and how it is useful for the attacker to know how many ports are open on the victim's screen. The students used a tool called nmap or Network Mapping for port scanning. In order to understand port scanning they first had to understand the 'TCP 3-Way Handshake'. The attendees then studied 'Symmetrical and Asymmetrical Cryptography'. The fundamental difference between the two is that in Symmetrical Cryptography, only 1 key is involved whereas, in Asymmetrical Cryptography, 2 keys are involved, one public and one private. The session was concluded with hashing and password hash cracker. The workshop was very informative and engrossing. All these interesting and new things were taught to the students. IETE-SF then thanked Hardik and Manan for their valuable time and contribution and thus ended the workshop.

Photographs of the Event:

Outcomes:
- The students understood the basic concepts of Ethical Hacking.
- They got well versed with various software required for Ethical Hacking.

Program Outcomes mapped (Please tick the mapped POs):

PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
1	1	1	1	1	1	1	1	1	1	1	1

Program Specific Outcomes mapped (Please tick the mapped PSOs):

PSO1	PSO2	PSO3	PSO4
1	1		

Department of Electronics and Telecommunication

4.3 Seminar: Carrier Guidance

Speaker: Mr. Sukrut Bhushan

Date of the Seminar: 23rd September, 2022.

Association of Speaker: Direct (Marketing and Sales), Inspirus international studies.

No. of Participants: 107 Students

Participants: SE and TE Students

Objectives of the activity:

To provide guidance for various opportunities related to higher studies, concern entrance test, courses.

Contents:

A master's degree is a specialised academic course of study designed to sharpen your professional skills in the field of your choice. Master's programmes can last anywhere from one to three years, depending upon the particular course you've chosen. During your master's program, you can expect to take classes

and exams, and, in some cases, you may conduct research and write a thesis or dissertation. Depending upon the school you're attending, you may also have the option of teaching, which can help defray the cost of your tuition as well as provide you with relevant experience. Therefore, to make students aware of master's programmes and how they can pursue them, IETE-SF conducted a seminar on "Study Abroad After Engineering". It was a one-day event. The seminar took place on September 23rd, 2022 and was a huge success, with over 100 participants attending the seminar. The session started with good enthusiasm among the attendees to know more about the master's programme under the guidance of the speaker.

Firstly, the speaker for the day, Mr. Sukrut Bhushan, introduced himself, sharing his experience in the education industry and working for one of the Fortune 500 companies. The speaker, who had experience of almost 10 years in that field and 7.5 years as a motivational speaker in colleges, then introduced the term GRE among the students. The Graduate Record Examinations (GRE) is a standardised test that is an admissions requirement for most graduate schools in the United States, Canada, and a few other countries. The GRE aims to measure verbal reasoning, quantitative reasoning, analytical writing, and critical thinking skills that have been acquired over a long period of learning. The importance of a GRE score can range from being a mere admission formality to being an important selection factor.

Following a brief introduction to the GRE, the speaker discussed various other tests, such as the TOEFL (Test of English as a Foreign Language) and IELTS (International Language Testing System), that are required for international admission. The Speaker then gave an idea about courses offered in various countries and which country would be best for a particular course. The attendees were also given a brief explanation of the total expenditure that would be required to complete their masters from various countries. IETE-SF then thanked Mr. Sukrut Bhushan for his valuable time and contribution and thus ended the seminar.

Photographs of the Event:

Mr. Sukrut Bhushan giving overview of the GRE, TOEFL, and IELTS.

Mr. Sukrut Bhushan explaining the current trends in various countries.

Outcomes:

- The students comprehended what the GRE is and why it is required to pursue a master's degree from a foreign university, particularly one in the United States of America.
- They further understood the various sections of the GRE and their respective weights.

Program Outcomes mapped (Please tick the mapped POs):

PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
						1	1	1			1

Program Specific Outcomes mapped (Please tick the mapped PSOs):

PSO1	PSO2	PSO3	PSO4
1	1	1	1

5. ACHIEVEMENTS

5.1 Faculty Publications- Conferences / Journals

Conference Publication

Sr.No	First Author	Paper Details	Indexed by
1	Aarti G. Ambekar	Aarti G. Ambekar, Tejal tirodakr, Venkata A P Chavali, Amit A. Deshmukh, "Slot Loaded Circular Microstrip Antenna for Circular Polarized Response," in 5th IEEE International Conference on Advances in Science and Technology (ICAST)-2022 organized by KJSIEIT, 2 - 3 Decemebr 2022.	Scopus
2	Venkata A P Chavali	Venkata A. P. Chavali, Tejal tirodakr, Aarti Ambekar, Amit A. Deshmukh, "Analysis of Broadband Single Layer Gap-Coupled Shorted Rectangular Microstrip Antenna,", in 5th IEEE International Conference on Advances in Science and Technology (ICAST)-2022 organized by KJSIEIT, 2 - 3 Decemebr 2022.	Scopus

Journal publication

Sr.No	First Author	Paper Details	Indexed by
1	Venkata A P Chavali	Venkata A. P. Chavali and Amit A. Deshmukh, "Multi-Resonator Wideband Designs of Semi-Circular Microstrip Antennas," IETE Journal of Research,1-8, September 2022.	Scopus
2	Venkata A P Chavali	Venkata A. P. Chavali Amit A. Deshmukh , "Wideband and Circularly Polarized Designs of Modified E–Shape Microstrip Antennas for GSM and GPS Applications," Progress In Electromagnetics Research C, Vol. 121, 107-125, July 2022.	Scopus
3	Venkata A P Chavali	Venkata A. P. Chavali, Amit A. Deshmukh, "Rectangular microstrip antenna loaded with offset U-slot and rectangular slot for wideband and circular polarized response in GSM 900 and GPS L5 band applications," Sādhanā, Vol. 47, No. (4):1-4, December 2022.	Scopus

5.2. Interaction of faculty with outside world

FDP/ STTP/Webinar/Workshop attended by Faculty Members:

Sr.No	Name Of Faculty	Details of Workshop/ Webinar/STTP/FDP	Date / Year of Event
1	Dr. Poonam Kadam	One-week international FDP on Emerging trends and Research opportunities in Computer Science.	31st Oct to 5th Nov 2022
2	Dr.Venkata A P Chavali	Completed UGC aaproved short term Professional Development Programme on 'Implementation of NEP2020 for University andCollege Teachers' conducted by IGNOU and obtained **A+ grade.**	9th - 17th September,2022
3	Dr. Aarti G. Ambekar	UGC aaproved short term Professional Development Programme on 'Implementation of NEP2020 for University andCollege Teachers' conducted by IGNOU	9th - 17th September,2022
4	Dr. Vishakha Kelkar	ISTE approved Online/SF-STTP/FDP Programme on Artificial Intelligence Towards Data Science Applications	27th June 08th July 2022
5	Dr.Venkata A P Chavali	Modern Digital Electronics webinar byMcGraw Hill	25th July 2022
6	Dr.Venkata A P Chavali	Work shop on on 5 G wireless Communication by Pantech E-learning	9th September 2022
7	Dr. Aarti G. Ambekar	Webinar on Data Science and Machine Learning using Python by McGraw-Hill	23rd September 2022
8	Dr. Aarti G. Ambekar	Workshop on on 5 G wireless Communication by Pantech E-learning	9th September 2022
9	Yukti Bandi	IISC advanced level training	2nd August 2022
10	RanjushreePal	Online FDP on Linear Algebra by SPIT, Andheri	26/07/22- 26/08/22
11	Yukti Bandi	IIA Online basic Training Certificate	July 2022
12	RanjushreePal	Conducted Advanced session on MATLAB under IETE SF, DJSCE	26/09/22

5.3. NEPTEL/COURSERA Courses completed by faculty members:

Sr.No	Name Of Faculty	Details of Workshop/ Webinar/STTP/FDP	Date / Year of Event
1	Dr. Ameya A. Kadam	NPTEL-Essential Mathematics for Machine Learning	Jul-Oct 2022
2	Dr. Ameya A. Kadam	NPTEL-Signal Processing for mm Wave communication for 5G and beyond	Jul-Oct 2022
5	Dr. Sunil Karamchandani	NPTEL- Patent Drafting for Beginners	July-August 2022
6	Dr. Sunil Karamchandani	NPTEL- Introduction to Film studies	July-Oct 2022
7	Yukti Bandi	NPTEL- Data Structures using python	July-September 2022
8	Revathi A S	NPTEL- Medical Image Analysis	July-Oct 2022
9	Anuja Odhekar	NPTEL- Industrial Internet of Things-12 weeks	June -December 2022
10	Anuja Odhekar	NPTEL-Stress Management-4 Weeks	June -December 2022
11	Dr. Aarti G. Ambekar	Coursera course on 'Introduction to AI' by IBM	26th August, 2022
12	Dr. Aarti G. Ambekar	Coursera course on 'What is Data Science' by IBM	26th August, 2022
13	Dr. Vishakha Kelkar	Coursera course on " Introduction to Data Analysis using Microsoft Excel"	17th August, 2022
14	Yukti Bandi	coursera course on "5 G for everyone "	01-07-2022
15	Yukti Bandi	coursera course on "Interfacing with Arduino"	13/07/2022
16	Shivani Bhattacharjee	Coursera Course " Industrial Internet of Things (IIoT)"	28/07/2022
17	Shivani Bhattacharjee	Coursera course on "5 G for everyone "	30/07/2022
18	Venkata A P Chavali	Coursera course on "5 G for everyone "	28/07/2022

5.4. Faculty Achievements

Name of Faculty	Event description	Date
Dr. Ameya A. Kadam	**National Level Topper** in 12 week NPTEL course on "Signal Processing for mm Wave communication for 5G and beyond"	Jul-Oct 2022
Dr. Ameya A. Kadam	Completed the Professional Development Programme on 'Implementation of NEP2020 for University and College Teachers' and obtained **'A+' Grade**	17-25, Oct., 2022
Dr. Venkata A P Chavali	Completed UGC approved short term Professional Development Programme on 'Implementation of NEP2020 for University and College Teachers' and obtained **'A+ grade'**	9th - 17th, Sept.,2022
Dr. Aarti G. Ambekar	Completed UGC approved short term Professional Development Programme on 'Implementation of NEP2020 for University and College Teachers' and obtained **'A grade'**	9th - 17th, Sept.,2022
Dr. Venkata APC	Completed Ph.D.	12.7.22
Dr. Sanjay Deshmukh	Completed Ph.D.	08.7.22
Dr. Aarti Ambekar	Completed Ph.D.	13.8.22
Dr. Ameya Kadam	Completed Ph.D.	13.8.22

5.5. Student's participation in various events

Sr. No	Name Of students	Technical Events	Date / Year of Event	Achievements
1	Srihari Narendra Kamath	"React Js Developer Internship Program" at Skill Safari.	Oct-22	successfully completed with Grade "A"
2	Nihal Shaikh	3 months of internship with VYAM in Mobile Application Development (Flutter)	May-Aug. 2022	successfully completed
3	Vatsal Zaveri	Online non-credit course authorized by Google and offered through Coursera on "Process Data from Dirty to Clean"	Aug 4, 2022	successfully completed
4	Vatsal Zaveri	Online non-credit course authorized by Google and offered through Coursera on "Prepare Data for Exploration"	July 29, 2022	successfully completed
5	Vatsal Zaveri	Online non-credit course authorized by Google and offered through Coursera on "Ask Questions to Make Data-Driven Decisions"	July 23, 2022	successfully completed
6	Vatsal Zaveri	Online non-credit course authorized by Google and offered through Coursera on "Foundations: Data, Data, everywhere"	July 18, 2022	successfully completed
7	shreys Tawade	DD Robocon 2022-Competition Organised by IIT Delhi in collaboration with Prasar Bharti	July 16-17 2022	successfully completed
8	Prasanna Nadkarni	Chakravyuh Quiz Contest organised by DJS Express Committee.	Oct-22	won 1st prize

Department of Electronics & Telecommunication Engineering   Academic Year 2022-2023

6 RESULT ANALYSIS

Academic Year   :   Acad . Year 2021-

2022Academic Session :   Semester IV

Exam Year   :   Acad . Year 2021-

2022Exam Session :   Semester IV

S.no	Module Abbreviation	Module Description	Total no of students appeared	Total no of students passed	% of passing in the subject
1	DJ19ECC402	Analog Communication	139	138	99.28
2	DJ19ECC403	Integrated Circuits	139	137	98.56
3	DJ19ECC401	Engineering Mathematics IV	139	133	95.68
4	DJ19ECC404	Electromagnetics and Wave Propagation	139	135	97.12
5	DJ19IHC1	Universal Human Values	139	139	100.00

Academic Year : **Acad. Year 2021-2022**

Academic Session: Semester VI

Exam Year : **Acad. Year 2021-2022**

Exam Session : **Semester VI**

S.no	Module Abbreviation	Module Description	Total no of students appeared	Total no of students passed	% of passing in the subject
1	DJ19ECC601	Digital Communication	138	138	100.00
2	DJ19ECC603	Fundamentals of Digital Image Processing	138	137	99.28
3	DJ19ECC604	Computer Networks	138	137	99.28
4	DJ19ECEC6012	Data Compression & Encryption	18	18	100.00
5	DJ19IBDAT501	Business Analytics for Industries	3	3	100.00
6	DJ19ECC602	Radiating Systems	138	136	98.55
7	DJ19ECEC6014	Artificial Intelligence & Machine Learning	120	120	100.00
8	DJ19IBAMT501	Application of Machine Learning in Industries	22	22	100.00
9	DJ19ILL2	Innovative Product Development-IV	138	138	100.00

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Academic Year   :   Acad. Year 2021-2022

Academic Session: Semester VIII

Exam Year   :   Acad. Year 2021-2022

Exam Session   :   Semester VIII

S.no	Module Abbreviation	Module Description	Total no of students appeared	Total no of students passed	% of passing in the subject
1	DECCILO8021	Project Management	5	5	100.00
2	DECCILO8023	Entrepreneurship Development and Management	2	2	100.00
3	DECCILO8022	Finance Management	117	117	100.00
4	DECCILO8029	Environmental Management	14	14	100.00
5	DECC801	RF Design	138	138	100.00
6	DECCDLO8041	Optical Networks	138	138	100.00
7	DJ19IBDAT501	Business Analytics for Industries	14	14	100.00
8	DJ19IBAMT501	Application of Machine Learning in Industries	12	12	100.00
9	DECC802	Wireless Networks	138	138	100.00
10	DJ19IBITT501	IOT for Industries	1	1	100.00

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7 PLACEMENT DATA

Total no. of Students Placed Company Wise = 84 (Including Multiple Placement Offers)

Sr. No.	Company Name	Salary Per	No. of Students Placed
1.	JP Morgan Chase	17.5	5
2.	Axxela	14.1	2
3.	ZS Associates	13.65	7
4.	ISS Governance	13.3	1
5.	Enfusion	13	1
6	Miko	12	1
7.	Fynd	10.2	2
8.	IDFY	8 to 10	1
9.	Think360	9.55	1
10	Quantiphi	9.5	4
11	Oracle Financial Software Services	8.8	1
12	Indus Valley Partner	8.75	2
13	Bill Desk	8.5	5
14	GEP Worldwide	8	1
15	Deloitte	8	4
16	Share Khan	8	1
17	Edelweiss Fianance	8	1
18	ICICI Lombard	8	2
19	Tresvista	7	2
20	Selec control	5 to 7	1
21	AMDOCS	6.5	2
22	LogisticsNow	6.5	1
23	Earnst & Youn	6.37	12
24	Ganit Inc.	6.0	2
25	KPMG	6.0	2
26	The Math Company	5.5	3
27	HDFC Bank	5.11	1
28	L&T Financial Services	4 to 5	1
29	Cognizant- GenC	4.5	2
30	LogIQids	4.0	2
31	TCS Ninja	3.6	11
Minimum CTC in LPA: 3.6 LPA		**Maximum CTC in LPA: 17.5 LPA**	
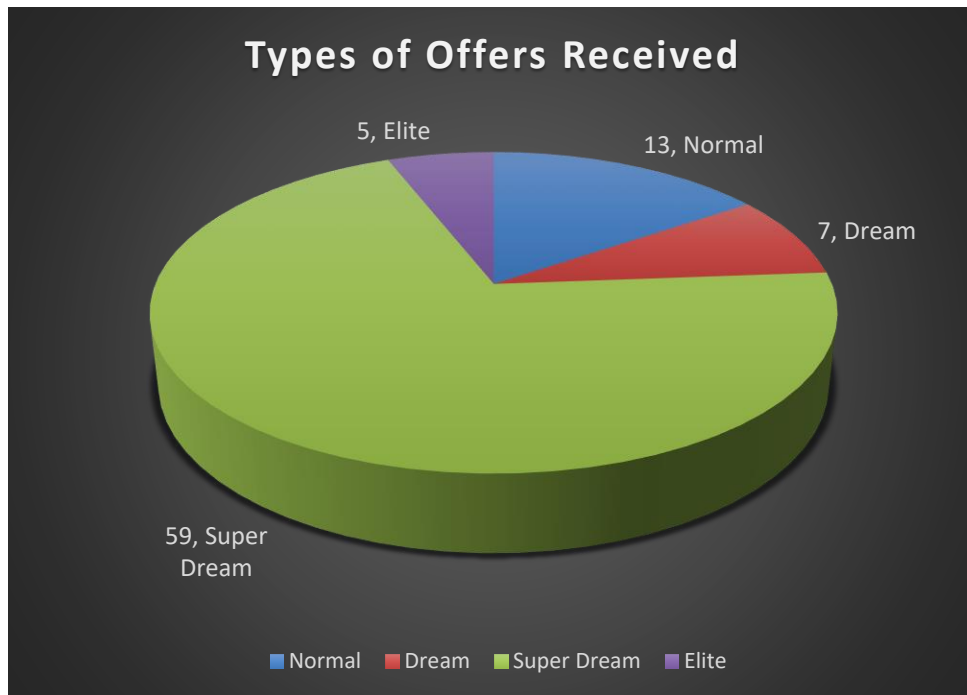
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Types of Offers Received

5, Elite

13, Normal

7, Dream

59, Super Dream

■ Normal ■ Dream ■ Super Dream ■ Elite

Ellite: >= 15 LPA, Super Dream: >7 LPA, Dream: 4.5 to 7.0 LPA, Normal: < 4.5 LPA

Multiple Offers Received

4

12

49

■ 1 Company ■ 2 Companies ■ 3 Companies

Total no. of Students placed Company wise = 109

ACADEMIC BULLETIN

Jan 2023- June 2023

Department of Electronics & Telecommunication Engineering

Prepared by:

Dr. Poonam Kadam   **Dr. Amit A. Deshmukh**

(Assistant Professor, EXTC, DJSCE)   **(Professor & Head EXTC, DJSCE)**

ACADEMIC BULLETIN
Period: 1ˢᵗ Jan 2023 – 31ˢᵗ June 2023

Department of Electronics & Telecommunication Engineering   Academic Year 2022-2023

1. ABOUT DEPARTMENT

1.1 Vision

To be a world class Institution for education, training and research in engineering, inculcating values and skills for sustainable development of the society.

1.2 Mission

- To provide competent faculty and an interactive learning environment along with worldclass infrastructure for nurturing professionalism & entrepreneurship in Engineers.
- To foster technical competence, research aptitude and environmental awareness amongstaspiring technocrats to develop sustainable engineering solutions.
- To provide a forum for active interaction between academia & industry, leading to continuous improvement in engineering education.

1.3 Vision of the Department

To develop technically competent and socially responsible Electronics and Telecommunication engineers capable of fulfilling expectations at indigenous and global levels.

1.4 Mission of the Department

- To provide a conducive educational environment for students by providing good infrastructural facilities, knowledge base and excellent faculty support.
- To provide a strong foundation of core knowledge and exposure to research culture.
- To motivate learners to acquire adequate professional and soft skills, to develop personalitytraits and eventually transform them as life-long learners.
- To strive and achieve practical exposure by maintaining good rapport with industry andprofessional network.

1.5 Program Specific Outcomes (PSOs)

- To develop knowledge in the domain of signal analysis and processing and provide a foundation to numerous other courses that deals with signal processing applications.

- To develop basic and applied knowledge of the architecture and assembly language programming for microprocessor/microcontroller-based systems, along with the peripheralinterfacing.

- To provide an in-depth understanding of electromagnetics, transmission lines and antennaconcepts along with microwave devices used for RF and microwave applications.

- To develop knowledge of the fundamental techniques related to generation, transmission and reception in communication systems for a wide range of wired and wireless applications along with revolutionary technology developments.

1.6 Program Educational Objectives (PEOs)

- **PEO1:** To prepare learners for graduate studies by providing strong foundation of basic sciences, computer programing and thus, develop analytical aptitude, and problem-solving abilities.

- **PEO2:** To develop a fundamental understanding of electronic & integrated circuits, communication systems and allied disciplines.

- **PEO3:** To develop core competency and expertise in the diverse areas of communication covering Signal processing, Electromagnetic Engineering, Embedded Systems, Computer Communication and Advanced Wireless Networks domains.

- **PEO4:** To inculcate competencies and aptitude in extending acquired technical knowledge to solve real life issues with high professional and ethical standards.

- **PEO5:** To develop proficiency in soft skills and deliver adequate personality traits to enable the pass outs to pursue higher education, to find competitive employment opportunities and/or pursue entrepreneurial ventures.

1.7 Department Information

- Started in the year 1999 with the intake of 30 and which was increased to 60 in the subsequent year.

- The intake was increased to 120 in the Academic Year 2010 – 11.

- In the Academic Year 2011 – 12, Department has started M.E. Program in Electronics & telecommunication with an intake of 18 students.

- For the first time Department got NBA accreditation for two years from January 2013. In second Outcome based evaluation, Department got NBA accreditation for three years from July 2017.

- The Department started with Ph.D. program in Academic Year 2015 – 16 with an intake of 10 students.

- The department is having highly qualified, experienced and dedicated faculties and supporting staff.

- Well-equipped labs and fully air-conditioned classrooms with projectors.

2. ADMINISTRATION

IETE COMMITTEE

Dr. Amit Deshmukh   Dr. Anuja Odhekar

PROJECT COORDINATOR

Dr. Amit Deshmukh   Dr. Ameya Kadam

DEPARTMENTAL LIBRARY

Dr. Amit Deshmukh   Prof. Archana Chaudhari

ALUMNI COMMITTEE

Dr. Amit Deshmukh   Prof. Ranjushree Pal

NBA CORE COMMITTEE

Dr. Amit Deshmukh   Dr. V. V. Kelkar (PC/NC)

Dr. Ameya Kadam   Dr. Venkata A. P. Chavali

AUTONOMY COMMITTEE

Dr. S. B. Deshmukh   Dr. Poonam A. Kadam

TIME-TABLE COMMITTEE

Dr. Venkata APC   Prof. Archana Chaudhary

PLACEMENT COORDINATOR   **NPTEL and IBM COORDINATOR**

Dr. Aarti Ambekar   Dr. V. V. Kelkar

Department of Electronics & Telecommunication Engineering   Academic Year 2022-2023

3. IETE- SF

The Electronics and Telecommunication Department of Dwarkadas. J. Sanghvi College of Engineering presents Institution of Electronics and Telecommunication Engineers- Student Forum (**IETE-SF**). The student chapter with a working force committee of 22, consisting of **second year** and third **year students**, hosted a few of the most quintessential and technically challenging events. A membership drive was conducted at the start of the year with an overwhelming response. (www.djsceietesf.com)

IETE Organizing Committee Structure

IETE SF Branch Counsellor: - Dr. Anuja Odhekar

Chairman	Kushal Katira
Vice-Chairman	Kenil Shah, Shrushti Thakar
Secretary	Sanika Dawre, Yash Shivramkrishnan
Treasurer	Shivam Desai
DJ-Strike Co-ordinator	Utsavi Lotia, Pranav Saraf, Urvashi Bhushan, Vidhi Tarak Nayak

Head Of Departments:	
Publicity	Sharath Pai
Marketing	Ayush Koul, Maahi Trivedi
Technical	Siddharth Nagaokar
Infotech	Sharmila Choudhary
Creatives	Sarthak Acharekar, Dhara Patel
Events	Hitarth Sharma
Book Bank	Disha Goel
Component Bank	Aditya Patil

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3.1 Value Added Program

Book Bank

IETE-SF provides the students with a book bank facility where they can issue reference books at nominal rates for the entire semester. Students who want a better insight into the subject avail this facility as these reference books aid in developing a good understanding of the topics and enable them to consolidate their foundation of the subject. Book bank has more than 60 book titles as per the syllabus requirement for 3rd to 8th semester. This activity not only motivates students to use reference books prescribed in syllabus but also explore them in the library management system. The alumni who worked as "Book-Bank" coordinators received paid assistance-ship in the library during their master program.

Component Bank

IETE-SF provides a component bank facility where students can borrow electronic components which they require for executing multiple projects both in and outside of the curriculum. They can utilize the facility by initially paying 50% of the cost and getting a refund of 20% on returning the components, provided that they are undamaged.

Department of Electronics and Telecommunication

4. DEPARTMENT ACTIVITIES UNDER IETE-SF

4.1 Industrial Visit to GMRT

Date: 3rd February, 2023
No. of Participants: 49 TE students

Objective of the activity:
- To expand the knowledge of students in the field of Telecommunication.
- To impart wisdom about radio astronomy and receiver technology.

Contents:
An Industrial Visit was conducted to GMRT Pune, Khodad district on 3rd February 2023 as a part of Radiating Systems course in semester VI with the aim of enlightening the students about the functioningof GMRT. The trip was accompanied by esteemed faculties, Dr. Amit A Deshmukh (Head of EXTC Department) and Dr. Rajendra Khavekar (Training and Placement Officer) alongside students. The GMRT (Giant Metrewave Radio Telescope), which is the world's most powerful telescope operates at low radio frequencies, is used to study radio signals with metre wavelengths from objects in the Milky

Way and other nearby galaxies. In recognition of its innovative engineering, cutting-edge

technology, and scientific contributions to the field of radio astronomy, it has received numerous accolades and is now the third Indian scientific facility to receive the IEEE Milestone. It is used by radio astronomers from across the world to study our Universe and observe different astronomical objects such as HII regions, galaxies, pulsars, supernovae, and Sun and solar winds. Radio astronomy is the field of sciencein which the radio waves generated by objects and celestial bodies in space are collected by a receiver and utilised to determine the object in detail. This information is then used for space exploration and thediscovery of resources on planets, stars, and other celestial bodies.

GMRT is a receiving station where sources are all the natural astronomical bodies. Recently, GMRT hadbeen the talk of the town as Scientists had made a groundbreaking discovery by capturing radio signals from a galaxy located almost 9 billion light-years away from the Earth. This was the first time that a signal of this nature had been received from such a distance.

The antenna is a parabolic dish with an outer diameter of 45m. To make it lightweight, meshes of steel are used. The outer mesh has dimensions of 15m x 15m and the inner mesh has dimensions of 5m x 5m.The azimuth angle of this antenna is ±270° and the elevation (solid angle) is 18° - 90°. The antenna is equipped with a boat-shaped structure between the parabolic dish and servo motors to balance the Centreof Mass of the parabolic dish. A BLDC (Brushless DC) servo motor is used for the rotation of the antennain the direction of the source. The antenna is painted every five years to prevent corrosion. The resolutionof an antenna is directly proportional to the size of the antenna. But practically designing antennas withhuge sizes is not a viable solution. Therefore, 30 small parabolic antennas are spread over an area of a 25km radius. All of these antennas receive signals from one source at a time.

In order to receive signals, fiber optic cables are used to feed the electrical signals. It can be used in eithersingle or multimode mode. For antenna remoting applications, a single mode is used as it supports longerlink distances and faster transmission of signals since a single mode is used. Signal loss is very low as the core used has a diameter of 1310 nm. In addition, walls are built around the antenna to prevent the ground propagation of signals.

The feeds presently available are Band-5 (1000-1460 MHz), Band-4 (550-850 MHz), Band-3 (250-500 MHz), and Band-2 (125-250 MHz) feed. The reflecting surface is formed by a wire mesh and the efficiency of the antennas varies from about 60% to about 40%, from the lowest to the highest frequency.Signals from two orthogonal polarizations are brought to the control room from each antenna, over opticalfiber. The native polarizations for all receiver systems are circular, except for the Band-5 system, whichdelivers linear polarizations.

When receiving signals, these antennas have to be synchronized in time with the source for the sound reception of signals. Hence, the antenna is rotated according to the source. Once the reception of the signal is completed, the antenna calibrates itself with the reference source every 20 mins. By default, the antenna completes one rotation every 30 mins. For perfect signal reception, counter-rotation is providedby the servo motors. After receiving the signals, correlation is performed to remove noise.

Frequency of operation and gain:
This antenna operates in the frequency range of 100-1500 MHz. The maximum gain of this antenna is40dB. Gain is controlled by using the parabolic dish's inner and outer meshes.

GMRT has played a crucial role in the exploration of space and has always been at the forefront to collectdata to discover the mysteries of space.

Photographs of the Industrial Visit:

Parabolic dish with an outer diameter of 45m

TE Batch 2024

Miniature Design

Explanation of Working of Antenna

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4.2 Alumni Meet 2023

Date: 11th February, 2023
No. of Participants: 50 SE and TE students

Objective of the activity:
- To connect students to the alumni of the EXTC Department.

Contents:

The alumni meet was a grand event organized by DJSCE IETE-SF on 11th February, 2023 that broughttogether former students and faculty members of the institution. The atmosphere was filled with excitement and nostalgia as people reconnected with their old friends and reminisced about their time atthe college.

The meet began when some alumni who couldn't attend in person joined online via Microsoft Teams. Professor Ranjushree Pal, our alumni coordinator was present to greet our respected alumni. Everyone was delighted to see each other after a long time. Everyone then introduced themselves and what they were currently doing. After introductions, some of the students had some dilemmas about placements and internships, to which one of the alumni pointed out that networking is very important for internships.In fact, that is the famous way most people get internships. Another doubt

that was raised was, "Is MBAeffective?" One alumnus recommended working for 2-3 years before considering a masters. Meanwhile,other alumni advised pursuing post-graduate studies immediately following your undergrad, as it may bedifficult to return to studies after 2-3 years. Knowing your basics is essential to pursuing your master's. They advised us to start building our resumes as soon as possible and to start making connections throughany platform.

They discussed the challenges they faced after and during their under-graduation and how they overcamethem, and they encouraged the students to never give up on their dreams. By this time, alumni who couldjoin in person had come. The room was quickly filled with happy faces. Everyone had pursued their careers in different directions so they were informed about the common mistakes that are usually made so that we could avoid them and learn from their mistakes. They also reminded us of all the problems ordifficulties they faced and the only solution to all of them was to stay focused and determined to achieveyour goal. Everything is manageable if you believe that the amount of work you put towards your goal, you will receive the output you intended to get. They assured us that at our age being confused and not knowing what to do ahead is okay. Building your life skills and communication skills is very important during our age. This age is for establishing life-long friendships and to make mistakes because that is how you grow and know. Going through that painful process is how you will end up knowing what is right for you and at the end, it is going to be worth it.

For one alumnus, engineering was all about selling something, it was either your product or yourself. It is all about the brand that you build for yourself that will get you in good places, you have to showcase yourself. An alumnus stated that start doing something, anything you will definitely find what makes youhappy and can see yourself doing for a good amount of your life. Engineering is not just about academicsor placement, it is about creating perspective and advised that we should start creating perspective insteadof just thinking about it. Only having one skill is not going to help you widen your horizon. There are going to be times where you are going to have to do a lot of things by yourself which you are doing for the first time but in the outside world or in the industrial world, everybody expects you to know it. Now is the time you have to build your personality, soft skills and explore everything. It can be overwhelmingbut it is definitely worth it at the end of it all.

The teachers were immensely proud of what everyone had become. They were thrilled to once again talkto them. Overall, the alumni meet was a fantastic opportunity for everyone to come together, reminisce,and celebrate their shared experiences.

Photographs of the session:

Alumni addressing the doubts

Alumni interacting with the students

Outcomes:

● Students connected with the alumni of the Department and gained knowledge about various fieldsin which they can pursue their career.

4.3 Seminar on Future Trends in VLSI Technology

Speaker of the event: Mr. Keval Kamdar
Date: 10th February, 2023
No. of Participants: 32 SE and TE students

Objective of the activity:
- To expand the knowledge of students in the field of VLSI.
- To impart wisdom about future trends in VLSI Technology.

Contents:

The field of VLSI technology has been a rapidly growing area of study and research for the past several decades. It involves the integration of a large number of transistors onto a single chip, allowing for the creation of compact and powerful electronic devices. The increasing demand for higher performance and energy-efficient electronic devices has fuelled the development of VLSI technology, leading to remarkable advances in fields such as mobile devices, computing and communication.

In order to share the latest developments in the field, a seminar was organized by Dr. Poonam Kadam wherein Mr. Keval Kamdar, an alumnus of the EXTC department, shared his valuable insights on VLSI technology and its future in a developing country like India. The first subject he broached was that of how students today tend to gravitate towards software-oriented fields when understanding the working of the underlying hardware that executes the code is just as important, if not more. Software engineers who understand hardware as well are the need of the hour.

Next, he enlightened the participants about how chips or semiconductor technology is the next big thing. India is quite far-behind in this field right now and still has a long way to go. He also acknowledged the progress the country has made in advancing semiconductor technology and predicted the possibility of a large number of jobs being created in this arena within the next few years. He mentioned that people today have a lot of information at the tip of their fingers but to truly take advantage of that goldmine, we need to have computing systems powerful enough to do just that. He later presented an example that surely all students could very well relate to. He mentioned how a graphics processing unit (GPU) performs multiple operations at once in order to render the frames of whatever beautiful games you play.

As an alumnus, he also spoke to the students about the importance of a master's degree and how everyone should opt for one if the situation permits. Due to the fact that he completed his masters from Georgia Tech, he was also able to make the students aware of the cut throat competition one faces in the top schools but also emphasized on. After a brief but enlightening talk about what to expect and what to look for while picking a master's course, the floor was opened up for questions.

While answering the plethora of questions that the curious audience had generated, he also mentioned how the United States is already in a state of recession, the effects of which will be apparent sooner or later in India as well. According to him, investing in and developing chip production in the country would lead to the creation of thousands of jobs. He gave students a brief idea of what it was like to work at the memory unit of a tech giant like Intel. On being asked what the most promising aspect of VLSI tech is, he answered that it was ferromagnetic memory units.

Overall, the session was quite informative and engaging. The students listened with rapt attention and asked a lot of doubts in order to learn more about the topic at hand

Photographs of the session:

Speaker enlightening the students

Speaker answering doubts of the students

Outcomes:
- Students acquired information about the scope that VLSI technology has to offer.
- Students understood the importance of the VLSI field and its future trends.

Program Outcomes mapped (Please tick the mapped POs):

PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
3			3		3		3			3	3

Program Specific Outcomes mapped (Please tick the mapped PSOs):

PSO1	PSO2	PS03	PSO4
3			3

4.4 Seminar on How to write a Technical Paper

Speaker of the event: Dr. Ameya Kadam**,** Assistant Professor, Dept. of EXTC Engineering, DJSCE.
Date: 15th February, 2023
No. of Participants: 52 SE and TE students

Objective of the activity:
- To make the students understand the structure of a technical paper.
- To highlight the importance and purpose of writing a technical paper.

Contents:
Technical papers help engineers create leadership in a competitive environment and overcome project hurdles. Technical Papers cover a wide range of subjects, such as combustion processes, simulation & modelling, and test techniques, in-depth test results, comparative studies, and methodology. Therefore, tomake students aware about how a technical paper should be written, IETE-SF Conducted a Session "Workshop on Technical Paper Writing". It was a one-day event. The Session took place on 15th February, 2023. The Session was very informative and interactive for the attendees. The Session startedwith good enthusiasm among the under the guidance of Professor Ameya Kadam.

A technical paper consists of title, abstract, keywords, introduction, methodology, findings, conclusion,acknowledgement and references. The title is the main attribute of a paper. It Should be

meaningful, concise, specific and informative and not generic. It should not be too long nor too short as the title is read by thousands of people but the paper is read by only a handful. The title should capture the fundamental nature of experiments and findings. Abbreviations, jargons, etc should be avoided in the title. The titles could be of various types such as Indicative, Informative, etc.

Next is the abstract. The abstract is about what has been achieved in the paper and is like a mini summaryof the paper. It should be specific and precise and should talk about the key elements of the paper. It should not consider any acronyms as the abstract is the selling point of the paper. It is advised that the abstract should be written at the end after the research work is completed. Then comes the Introduction.The introduction should contain four paragraphs and the abstract has to be described in more detail in the introduction.

Then comes the main part of the technical paper, which is the research work which has been done. Thereshould be a novelty in the research and it should be properly mentioned in the paper. The body is the partwhere all the diagrams, graphs have to be included. The research should be solid as poor research can't be covered by brilliant writing. The results and discussion section should come after the methodology has been done. The results and discussion part contains what one has gained through their research, whathave they implemented.

The conclusion of the paper is what all findings have been done in the paper and it should be to the point.Words such as we did this and that should be avoided in the conclusion. The acknowledgement section should consist of a token of thanks to the people who have helped you with the research work or those who have funded you. In the references section, a lot of websites should not be mentioned rather researchpapers dated until last decade can be included.

Lastly, the speaker showed an example of a technical paper and highlighted all the key points which hadto be included. The speaker also mentioned a few websites for making good quality graphs and diagrams.
IETE-SF then thanked Professor Ameya Kadam for his valuable time and contribution and thus endedthe seminar.

Overall, the session was quite informative and engaging. The students listened with rapt attention andasked a lot of questions in order to learn the process of writing a technical paper.

Photographs of the session:

Speaker showing an example of a technical paper

Token Of Thanks

Students who attended the seminar

Speaker

Outcomes:
- Students learnt the way a technical paper is written.
- Students understood the importance of a technical paper.

Program Outcomes mapped

PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
1	1	1	2	1		1		1	1	1	1

Program Specific Outcomes mapped

PSO1	PSO2	PS03
2	2	2

4.5 DJSCE IETE-SF in collaboration with DJSCE Trinity

CLASH OF CODES
Code. Optimize. Collaborate

Date: 4th-5th March, 2023

No. of Participants: 240 SE, TE and BE students in offline mode and 130 in online mode

Objective of the activity:

● To foster innovation, creativity, and collaboration among participants, and to create a tangible solution to a specific challenge within a limited timeframe.

● To create an innovative environment that allows participants to explore new ideas, push boundaries, and create something meaningful

Clash of Codes Organizing Team

Contents:
Description of the event:

A hackathon is an event in which individuals or teams come together to collaborate and work on a specificproject or problem. Hackathons also provide an opportunity for participants to learn new skills, networkwith other professionals in their field, and gain exposure to potential employers. Clash of Codes was a 24-hour hackathon organized by DJSCE IETE-SF in collaboration with DJSCE Trinity which gave the participants a chance to create real world applications. The hackathon also promoted collaboration, encouraged students to pursue technical careers, and boost creativity. The hackathon was held in hybridmode on 4th and 5th March, 2023. We were thrilled to have over 1300+ registrations from individuals spanning over 260 teams of various backgrounds and skill-sets across

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various cities in India, out of which60 teams were shortlisted for the offline mode.

DAY 1 - 04/03/23

The participants had to report at the college by 8 a.m. on 4th March, 2023. As the participants started settling to their assigned classrooms, at least one member from each group had to be present in the Seminar Hall for the Inauguration Ceremony, which took place at 10 a.m., where the participants were addressed by our respected Principal, Dr. Hari Vasudevan Sir, Vice-Principal, Dr. Manali Godse Ma'am and Branch Counsellor of the EXTC Department, Dr. Anuja Odhekar Ma'am. Meanwhile, the problem statements were released at 9 a.m. and the participants were given the time to discuss and choose their desired problem statement.

The participants were then informed about the rules of the hackathon and other accommodations. At 11a.m., the problem statement preference form was released and the problem statements were allotted according to first come first serve basis so the participants had to be quick on their feet and choose theirfavoured problem statement. The allotted problem statements were released at 11:30 a.m. and that is when the coding began. The participants started discussing the solutions to their problem statements.

At 3 p.m., after lunch, there was a mentoring round for each domain where the mentors would advise theparticipants on what they should do in order to make their project better. They did not give out any ideasdirectly, they instead just nudged them in the right direction. The mentoring round went on till 5 p.m. and at 6 p.m., high tea was offered to all the participants. Keeping in mind the intense hard work that goes into solving modern software problems in a limited time frame, Recreational zones were set up in the seminar hall with provisions of e- games as well as a variety of board games. Additionally, a specialmusical jam session "Rhythmic Codes" was organized by DJS Beats and Clash Of Codes. They performed some classics, which boosted and revived the participants' spirits. The show went on till 9 p.m., when dinner was given out to all the participants. While some of the committee members were tired, the participants were now determined and focused to code. One of the participants happened to mention that most of the coding happens after 2 a.m. Sleeping arrangements were made for all the participants. The team members took turns in sleeping, some slept at night while the others were up coding.

DAY 2 - 05/03/23 The next morning, the participants were dedicatedly working on their projects and soon, they were called for breakfast. The coding was going to go on till 12 noon on 5th March and it was not long before the judges arrived. They were directed to the Seminar Hall and then to their respected labs according to their domains where they had to judge the products of the participants. Each domain had two to three judges for judging the teams. After rigorously judging the 60 teams, the shortlisted final 16 teams were announced for the final round. The final round presentations began at 4 p.m. and after meticulously judging the teams, the overall winners and domain winners were announced at 8:30 p.m. and the winners were given certificates and cheques.

Overall Winners:

Position	Team Name	Participants Name	Prize Money	Affiliation
1	Mango DB	Hetvi Solanki Vinit Shah Avish Jain Drishti Dhingani	₹40,000	Students, DJSCE
2	Inspect Elements	Eshan Trivedi Aryan Parmar Kunal Chaturvedi Hussain Pettiwala	₹20,000	Students, KJSCE
3	Enemies of Syntax	Bhumika Mange Dishant Zaveri Pratham Bhoir Mihir Shinde	₹10,000	Students, DJSCE

Domain winners:

AIML:

Position	Team Name	Participants Name	Prize Money	Affiliation
1	Enemies Of Syntax	Jay Jain, Arihant Sheth Aryan Mehta ,Jash Bhatia	₹10,000	Students, DJSCE
2	Lambda Coders	Jay Patel, Jharana Solanki Kunal Joshi, Yuvraj Thakur	₹5,000	Students, DJSCE

WEB/APP:

Position	Team Name	Participants Name	Prize Money	Affiliation
1	O(1)	Mohammed Aamir Khan Jaydeep Jethwa Raviranjan Prasad Abhishek Mishra	₹10,000	Students, Thakur College Of Engineering
2	Mingwx86	Sachin Jangid Jash Doshi Manasi Ghutukade Aditya Surve	₹5,000	Students, Thadomal Shahani College Of Engineering

BLOCKCHAIN:

Position	Team Name	Participants Name	Prize Money	Affiliation
1	Spaghetti_Coders_1337	Shiv Bhonde Leander Dsilva Aadil Saudagar Ayush Shah	₹10,000	Students, DJSCE
2	Hogwatts	Yash Brahmbhatt Sakshi Pandey Keyur Pancholi Vashisth Zatakia	₹5,000	Students, DJSCE

Photographs of the event:

Participants coding

Judges

Judging going on

Presentations going on

Outcomes:
- Students gained exposure to real world problems, networked with individuals.
- Students collaborated with everyone, won prize money and also discussed new ideas to make this world a better place.

Program Outcomes mapped

PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
2	2	2	2	2	2	2	2	2	2	2	2

Program Specific Outcomes mapped

PSO1	PSO2	PS03	PSO4
2	2	2	2

4.6 **DJS STRIKE**

Date: 3rd April, 2023

No. of Participants: 200 SE, TE students

Objective of the activity:
- To bridge the gap between theoretical and practical knowledge.
- To upskill and equip students with technical paper writing and research abilities.

Contents:

DJS Strike 2023, the flagship event of DJSCE IETE-SF, was held on the 3rd of April, 2023. DJS Strike is a platform that aims to encourage students who are passionate about learning and implementing ideasin the form of technical projects. This event is an opportunity for students to broaden their horizons in their respective fields of interest and present their projects with conviction.

The DJS Strike competition entails projects in several domains ranging from software to hardware. From IoT to Database management systems, antenna designing, smart devices and microcontroller applications. The strike competition empowers students to build modern day solution to everyday problems. Over the fifth semester, students of the third year have ideated, designed and implemented their projectswith continued guidance from faculty members. Each project has been through several reviews by internal and external faculty for ensuring steady and continuous growth and progress at each stage. Afterthe project is implemented, a technical paper is written by the students. Students are trained on how to write these papers through workshops and faculty guidance.

On the day of the event, the participants started gathering at the premises at 8:30 a.m. to set up their

projects or models in their assigned labs. Once they had finished setting up, all the participants were required to attend the Inauguration ceremony in the Seminar Hall. The event began with an inspiring speech by the Respected Principal, Dr. Hari Vasudevan Sir. His words were the perfect motivation for theparticipants, just before they presented their projects. Following the speech, the Respected Vice Principal, Dr Manali Godse Ma'am, and the Respected Head of the EXTC Department, Dr. Amit Deshmukh Sir, unveiled the DJS Strike magazine.

The DJS Strike magazine consists of the technical papers of the projects that take part in the Strike competition. To evaluate the projects demonstrated by the students and aid in the difficult task of selecting the winners, the committee invited two judges for Strike. Their presence enriched the event, asthe stories and experiences they shared were inspiring and motivating for the students. After the inauguration ceremony, the participants were escorted to their respective classrooms for the judging process to begin.

Strike was held on the 4th floor. Each team had a time limit of 5 minutes for their presentation, followedby 2 additional minutes given to the judges for questions. This provided the judges with a better understanding of all the components and techniques used by the participants. The judges made sure to evaluate each one thoroughly. Five of the Strike projects were selected for participating in Spark. The participants of the Strike competition were allotted a time slot in which they had to present. The projects with a working model of their project had recorded a small demo of it. The judging took place from 10:30 a.m. to 2 p.m., after which lunch was arranged for all the participants. After lunch, it was time for the participants to receive the results. The results were announced after careful analysis and rigorous discussions among the judges. The list of winners is given below:

DJS STRIKE Winners:

Position	Project Name	Participants Name	Faculty Guide	Affiliation
1	Smart Irrigation using IoT	Viraj Deshmukh Shubh Pokarne Saili Mhapankar Kimaya Ved Kashish Patni Darshan Markar	Prof. Archana Chaudhari	DJSCE
2	Robotic Arm on Wheels: A Smart Solution for Industrial Automation	Lav Parmar Shivam Udeshi Nithik Pandya Sreekrishna Nair Soham Shetty Aman Panchal	Prof. Tushar Sawant	DJSCE
3	Footstep Power Generation Tile	Harsh Kumbhat Ishan Shrivastava Atharva Arya Shreedhar Barot Dhruv Panchal Saumya Thakkar	Prof. Rahul Taware	DJSCE

Congratulations to all the winners! The event was a huge success, and we thank all the participants for their efforts and hard work. We look forward to seeing you at the next DJSCE IETE-SF event.

Photographs of the event:

Participants showcasing their projects to the judges

Committee members with Strike faculty in-charge Prof. Yukti Bandi

Outcomes:

● Students gained exposure and explored various domains in the electronics andtelecommunication field.

● Students came under a common platform to demonstrate their projects and also understood theimportance of a technical paper.

Program Outcomes mapped (Please tick the mapped POs):

PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
2	2	2	2	2	2	2	2	2	2	2	2

Program Specific Outcomes mapped (Please tick the mapped PSOs):

PSO1	PSO2	PS03
2	2	2

4.7 <u>DJS SPARK</u>

Date: 3rd April, 2023

No. of Participants: 200 SE, TE & BE students

Objective of the activity:
- To bridge the gap between theoretical and practical knowledge.
- To upskill and equip students with technical paper writing and research abilities.

DJS Spark 2023: Bridging the Gap between Theory and Practice

DJS Spark 2023, organized by the DJSCE IETE-SF, was held on April 3rd, 2023, with the objective of bridging the gap between theoretical and practical knowledge among SE, TE, and BE students. The eventprovided an opportunity for students to showcase their technical project ideas and develop their researchand technical paper writing skills. The event attracted 200 students who were enthusiastic about learningand implementing ideas in their respective fields.

The event began with the participants setting up their projects or models in their assigned labs. Soon after, all the participants were present in the Seminar Hall for the Inauguration ceremony, which was inaugurated by the Respected Principal, Dr. Hari Vasudevan Sir. He delivered an inspiring speech that motivated the participants to give their best during their project presentations.

After the speech, the Respected Vice-Principal, Dr. Manali Godse Ma'am, and the Respected Head of

the EXTC Department, Dr. Amit Deshmukh Sir, unveiled the three magazines: DJS Ignite, DJS Spark, and DJS Strike. The DJS Ignite magazine consists of a yearly summary of events and some thoughts of students. The DJS Spark magazine consists of technical papers of the projects that take part in the Sparkcompetition.

The committee had invited two judges for Spark to evaluate the projects demonstrated by the students and aid in the difficult task of selecting the winners. The participants were escorted to their respective classrooms for the judging to begin. Spark was held on the 5th floor, and participants from colleges outside Mumbai participated in Spark through an online meeting.

The participants had a time limit of 5 minutes for their presentation, and then 2 more minutes were givento the judges for questions. The judges were very invested in all the projects and asked many questions to understand the components or techniques the participants had used.

After lunch, it was time for the results. The judges announced the winners after careful analysis and rigorous discussions. The winners of the DJS Spark competition were as follows:

DJS SPARK Winners:

Position	Project Name	Participants Name	FacultyMentor	Affiliations
1	Implementationof a Robotic Arm	Bhaumik Thakker Vedangi Gupte Taher Kapadia Om Gabani Ojas Chanakya	Prof. Mrunalini Pimpale	Dwarkadas J. Sanghvi Collegeof Engineering.
2	Real Time Package Delivery System	Annu Yadav Drashti ThakkarJatin Suvarna Harsh Rai		St. Francis Institute of Technology
3	Dual Axis SolarTracker with Weather Forecast	Om Singh Deivagna Vanani Om Shah Awadesh Thakur Darshil Shah Pratham Mishra	Prof. YuktiBandi	Dwarkadas J. Sanghvi Collegeof Engineering.
3	Detection and prediction of AirPollution using Machine Learning	Tushar Ghanvir		YCCE Nagpur

The DJS Spark 2023 event was successful in achieving its objective of bridging the gap between theoretical and practical knowledge. The participants were able to showcase their technical project ideasand develop their research and technical paper writing skills. The event provided an opportunity for students to broaden their horizon in their respective field of interest and present it with conviction.

The event was a learning experience for the participants as they received feedback from the judges on their projects. The judges shared their experiences and inspired the participants to continue to learn

and innovate. The event was also an opportunity for the participants to network and collaborate with studentsfrom other colleges and broaden their knowledge.

In conclusion, the DJS Spark 2023 event was a success in providing an opportunity for SE, TE, and BEstudents to showcase their technical project ideas and develop their research and technical paper writing skills. The event bridged the gap between theoretical and practical knowledge.

Photographs of the event:

Unveiling of the three magazines

Students demonstrating projects to the judges

Project Demonstration

Judges & Faculty Members

Outcomes:
- Students gained exposure and explored various domains in the electronics and telecommunication field.
- Students came under a common platform to demonstrate their projects and also understood the importance of a technical paper.

5. ACHIEVEMENTS

5.1 Faculty Publications- Conferences / Journals

Conference Publication

Sr. No	First Author	Paper Details	Indexed by
1	Ms. Revathi A S	Samyak Shah, Shikha Punjabi, Shweta Chavan, Revathi A S, K. Kavitha, A comparative study on Diabetic Retinopathy Detection and Classification, ICNTE-2023 ,FRCIT, Vashi in association with IEEE and IAS on January 20-21, 2023.	Scopus
2	Ms. Revathi A S	Dhruvin Rajesh Dungrani, Harsh Rajesh Lotia, Dhairya Vibhav Parikh, Revathi A S, Kavitha K, Detection and Classification of Diabetic Retinopathy using Deep Learning, ICNTE-2023, FRCIT, Vashi in association with IEEE and IAS on January 20-21,2023.	Scopus
3	Dr. Sunil Karamchandani	Pranav Hari Panicker, K. Shah and S. Karamchandani, "CNN Based Image Descriptor for Polycystic Ovarian Morphology from Transvaginal Ultrasound," International Conference on Communication System, Computing and IT Applications (CSCITA), Mumbai, India, 2023.	Scopus

Journal publication

Sr. No	First Author	Paper Details	Indexed by
1	Dr. Aarti G. Ambekar	Aarti G. Ambekar, Amit A. Deshmukh, "Low Profile Design Of Regular Shape Microstrip Antennas Backed By Fractal Slots Cut Ground Plane For Circular Polarized Response", Progress In Electromagnetics Research C, vol. 129, pp. 203-219, January, 2023, DOI: 10.2528/PIERC22112303	Scopus
2	Dr. Venkata A P Chavali	Broadband design of corner slot cut sectoral microstrip antenna. International Journal of Electronics Letters, pp. 1-10, February 2023.	Scopus

5.2. Interaction of faculty with outside world

FDP/ STTP/Webinar/Workshop/Online Courses attended by Faculty Members:

Sr. No	Name Of Faculty	Details of Workshop/ Webinar/STTP/FDP	Date / Year of Event
1	Ms. Yukti Bandi	One week FDP on Emerging technologies in Machine learning at DJSCE, Mumbai	30/01/2023 to 03/02/2023
2	Dr. Venkata A P Chavali	One week FDP on Emerging technologies in Machine learning at DJSCE, Mumbai	30/01/2023 to 03/02/2023
3	Dr. Venkata A P Chavali	participation in the seminar on "SOC to Solution: The Next Big Paradigm Shift" by Dr. Veena S Chakravarthi organized by IEEE-IISc, VLSI Chapter.	11/03/2023
4	Dr. Aarti G. Ambekar	One week FDP on Emerging technologies in Machine learning at DJSCE, Mumbai	30/01/2023 to 03/02/2023
5	Dr. Aarti G. Ambekar	participation in the seminar on "SOC to Solution: The Next Big Paradigm Shift" by Dr. Veena S Chakravarthi organized by IEEE-IISc, VLSI Chapter.	11/03/2023
6	Dr. Aarti G. Ambekar	Webinar on "Modern Power System Analysis", organized by MC Graw-Hill	28/02/2023
7	Dr. Aarti G. Ambekar	Webinar on "Millimeter-Wave and Terahertz Solutions in SiGe BiCMOS Technologies", organized by IEEE IISc MTT/AP-S Student Branch Chapter, in association with IEEE University Partnership Program, and IEEE Bangalore Section.	15/02/2023
8	Ms. Revathi A S	One week FDP on Emerging technologies in Machine learning at DJSCE, Mumbai	30/01/2023 to 03/02/2023
9	Ms. Bahar Soparkar	One week FDP on Emerging technologies in Machine learning at DJSCE, Mumbai.	30/01/2023 to 03/02/2023
10	Dr. Ameya A. Kadam	Hands-on-workshop - 5G: Advanced Antenna Design using Ansys HFSS organised by NPTEL	28/01/2023.
11	Dr. Aarti G. Ambekar	IP Awareness/Training program, organized by Intellectual Property Office and MoE's Innovation Cell, India	20/01/2023
12	Ms. Bahar Soparkar	NPTEL (NPTEL-AICTE FDP) Introduction to Soft Computing Duration: 8 weeks, Score: 83%.	Jan – Mar 2023
13	Dr. Venkata A P Chavali	NPTEL (NPTEL-AICTE FDP) Remote Sensing :- Principle and Applications Duration : 12 weeks, Grade:- Elite	Jan - April 2023
14	Dr. Aarti G. Ambekar	NPTEL (NPTEL-AICTE FDP) Remote Sensing :- Principle and Applications Duration : 12 weeks, Grade:- Elite	Jan - April 2023

15	Dr. Poonam Kadam	Reviewer for 4th IEEE Bombay Section Signature Conference (IBSSC-2022).	Dec 2022
16	Dr. Aarti Ambekar	Reviewer for the journal IEEE Access.	Jan 2023
17	Dr. Poonam Kadam	Reviewer for the Journal Microwave and Optical Technology Letters.	Feb 2023
18	Dr. Venkata A P Chavali	reviewer at the 4th International Conference on Communication Systems, Computing and IT Applications (CSCITA-23).	Mar 2023
19	Dr. Amit Deshmukh	Reviewer for the Journal Progress in Electromagnetics Research.	Jan 2023
20	Dr. Aarti Ambekar	Reviewer for the journal IEEE Access.	Jan 2023
21	Dr. Amit Deshmukh	Reviewer for the Journal of the Indian Academy of Sciences, SADHANA, Springer.	Feb 2023

5.3. Faculty Achievements

Name of Faculty	Event description	Date
Dr. Sunil Karamchandani	Best paper Award, 4th International Conference on Communication Systems, Computing and IT Applications CSCITA 2023. Signal Processing Track	31/03/2023 to 01/04/2023
Ms. Bahar Soparkar	NPTEL (NPTEL-AICTE FDP) Introduction to Soft Computing Duration: 8 weeks Score : 83%.	Jan – Mar 2023
Dr. Venkata A P Chavali	NPTEL (NPTEL-AICTE FDP) Remote Sensing :- Principle and Applications Duration : 12 weeks Grade:- Elite	Jan - April 2023
Dr. Aarti G. Ambekar	NPTEL (NPTEL-AICTE FDP) Remote Sensing :- Principle and Applications Duration : 12 weeks Grade:- Elite	Jan - April 2023

5.4. Student's participation in various events

Sr. No	Name Of students	Technical Events	Date / Year of Event	Achievements
1	Kenil Shah	JPMorgan's Code for Good Challenge 2023	24/06/2023 to 25/06/2023	won 1st place
2	Kenil Shah	ETHGlobal's International Hackathon Scaling Ethereum 2023	01/03/2023 to 31/03/2023	won 12,000/-
3	Kenil Shah	ETHGlobal Tokyo International Hackathon 2023	14/04/2023 to 16/04/2023	won 10,000/-
4	Kenil Shah	ETHGlobal Lisbon International Hackathon 2023	12/05/2023 to 14/05/2023	won 25,000/-
5	Kenil Shah	Spheron Dappathon International Hackathon 2023	22/04/2023 to 08/05/2023.	won 1st Place with INR 27,000/-
6	Abhay Rajesh Bhosle, Chaitanya Sawant	S.P.I.T. Peripherathon 2023 - AI Powered IoT Hackathon	07/05/2023	won 1st Prize
7	Shubh jatin pokarne, Viraj Deshmukh, Saili Mhapankar	S.P.I.T. Peripherathon 2023 - AI Powered IoT Hackathon	07/05/2023	won 2nd Prize
8	Viraj Deshmukh	Lines of Codes 5.0 (LOC 5.0) held at D. J. Sanghvi College of Engg.	12/03/2023	Best Innovation in LOC 5.0, Second place in Peripherathon 1.0
9	Bhuvan Bagwe	S.P.I.T. Peripherathon 2023 - AI Powered IoT Hackathon	07/05/2023	won 3rd Prize
10	Kodali Chaitra Thejasvi	Battle bot (Quarks robowars), Bits Goa (Quark)	24/03/2023	7th Nationwide
11	Prasanna Nadkarni	Chakravyuh Quiz Contest organised by DJS Express Committee 2023.		won 1st Prize
12	Sakshi Bhushan Mane	Cricket at Trinity, DJSCE	04/02/2023	1st place in cricket
13	Rushi patel	Long jump, DJSCE	04/02/2023	Long jump 3rd position in college
14	Rushi patel	Long jump, DJSCE	04/02/2023	3rd place in long jump
15	Tanmay Subhash Londhe	Cultural, Inter-departmental dance competition, DJSCE TRINITY.	17/03/2023	2nd place in group dance
16	Prasanna Nadkarni	Chakravyuh Quiz Contest organised by DJS Express Committee	03/02/2023	Winner of Quiz Contest

| 17 | Nainika Hadkar | Dance competition, Trinity DJSCE | 17/03/2023 | 2nd position |

5.5. NEPTEL/COURSERA Courses completed by Student's

Sr. No	Name Of students	Course completed	Date / Year of Event
1	Ujjwal Kumar Thakur	Data Science for Engineers, NPTEL by Swayam	Jan –March 2023
2	Devarsh Shah	Master Course in Buisness Fundamentals and Development, Udemy	11/04/2023
3	Manan Doshi	Marketing Analytics: Customer Value and Promotion Strategy, Udemy	14/04/2023
4	Vatsal Zaveri	Mathematics for Machine Learning, Coursera	07/02/2023

Department of Electronics & Telecommunication Engineering   Academic Year 2022-2023

8 RESULT ANALYSIS

Academic Year   :   Acad .Year 2021-2022

Academic Session :   Semester IV

Exam Year   :   Acad .Year 2021-2022

Exam Session   :   Semester IV

Overall Record	Female	Male	Total
No of students appeared for Examination	35	88	123
Total No of students passed	32	72	104
No of students failed with ATKT	3	16	19
No of outright failures	0	0	0
Thus % of result is	91.43	81.82	84.55

Academic Year   :   Acad. Year 2021-2022

Academic Session :   Semester VI

Exam Year   :   Acad. Year 2021-2022

Exam Session   :   Semester VI

Overall Record	Female	Male	Total
No of students appeared for Examination	39	100	139
Total No of students passed	39	94	133
No of students failed with ATKT	0	6	6
No of outright failures	0	0	0
Thus % of result is	100.00	94.00	95.68

Department of Electronics & Telecommunication Engineering   Academic Year 2022-2023

Academic Year   :   Acad. Year 2021-2022

Academic Session :   Semester VIII

Exam Year   :   Acad. Year 2021-2022

Exam Session   :   Semester VIII

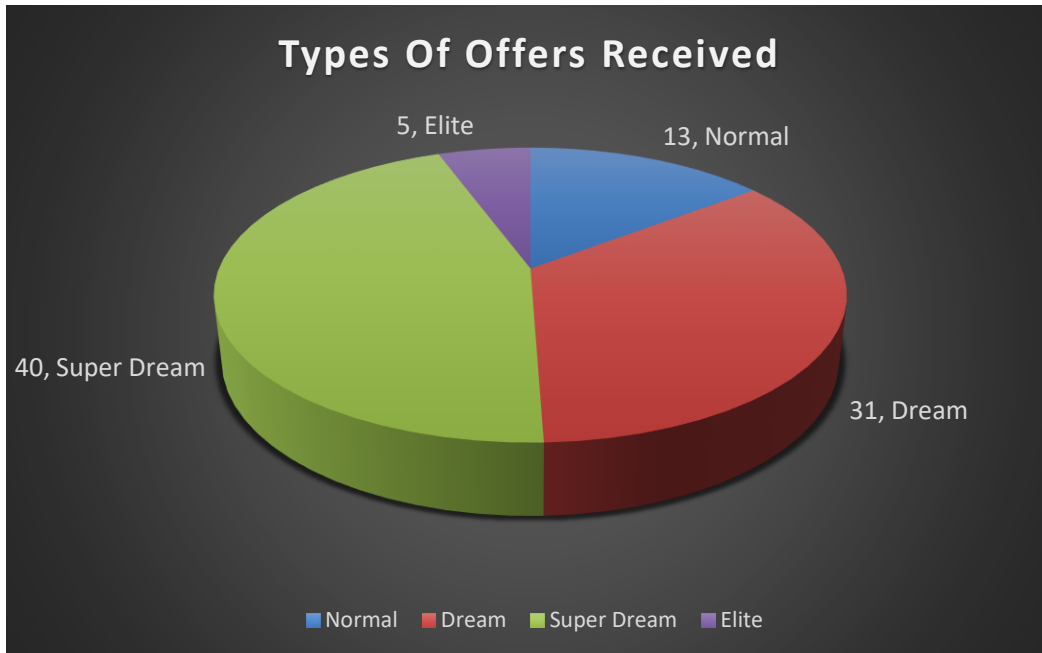| Overall Record	Female	Male	Total
No of students appeared for Examination	42	96	138
Total No of students passed	41	96	137
No of students failed with ATKT	1	0	1
No of outright failures	0	0	0
Thus % of result is	97.62	100.00	99.28

9 PLACEMENT DATA

Total no. of Students Placed Company Wise = 89 (Including Multiple Placement Offers)

Sr. No.	Company	CTC (LPA)	No. Of Students Placed
1	JP Morgan Chase	17.5	5
2.	Axxela	14.1	2
3	ZS Associates	13.65	7
4	ISS Governance	13.3	1
5	Enfusion	13	1
6	Miko	12	1
7	Forcepoint	11.6	1
8	Fynd	10.2	2
9	IDFY	10	1
10	ICICI Lombard	10	2
11	Think360	9.55	1
12	Quantiphi	9.5	4
13	Oracle Financial Software Services	8.8	1
14	Indus Valley Partner	8.75	2
15	Bill Desk	8.5	5
16	GEP Worldwide	8	1
17	Deloitte	8	4
18	Share Khan	8	1
19	Edelweiss Finance	8	1

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20	Tresvista	8	2
21	Selec control	5 to 7	1
22	AMDOCS	6.5	2
23	LogisticsNow	6.5	1
24	Earnst & Young	6.37	12
25	HDFC Bank	6.36	1
26	L&T Financial Services	6	1
27	Ganit Inc.	6.0	2
28	KPMG	6.0	2
29	Decimal Point	6.0	1
30	Philips Capital	6.0	1
31	The Math Company	5.5	3
32	Gandhi Automation	5.5	1
33	Cognizant- GenC	4.5	2
34	Squareyards	4.5	1
35	LogIQids	4.0	2
36	TCS Ninja	3.6	11
	Minimum CTC in LPA: 3.6 LPA	**Maximum CTC in LPA: 17.5 LPA**	

Types Of Offers Received

5, Elite

13, Normal

40, Super Dream

31, Dream

■ Normal ■ Dream ■ Super Dream ■ Elite

Ellite: >= 15 LPA, Super Dream: >7 LPA, Dream: 4.5 to 7.0 LPA, Normal: < 4.5 LPA

Multiple Offers Received

4

13

50

■ 1 Company ■ 2 Companies ■ 3 Companies